

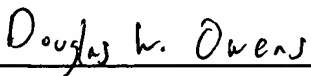


Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1965		SERIAL NO. 10/087,416	
 LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Jigish D. Trivedi et al.			
				FILING DATE February 27, 2002		GROUP 2811	
U.S. PATENT DOCUMENTS							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
	AR		Stanley Wolf, Ph.D. (Silicon Processing For the VLSI Era - Volume II) ©1990 by Lattice Press; pages 348-363				
	AS						
	AT						
EXAMINER 			DATE CONSIDERED 1/8/03				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							